



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Debanjan Mukherjee; Jishnu Bhattacharjee; Abhijit Phanse
Assignee: Scintera Networks, Inc.
Title: Multi-Layer Symmetric Inductor
Serial No.: 10/718,449 Filing Date: November 19, 2003
Examiner: Unassigned Group Art Unit: Unassigned
Docket No.: M-15267 US

Irvine, California
February 23, 2004

Mail Stop IDS
COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Dear Sir:

Pursuant to 37 C.F.R. §1.97, Applicants wish to call the following documents (copies enclosed) to the attention of the Examiner.

1. A PTO form 1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, or
3. an admission that the information cited herein is, or is considered to be material to patentability as defined in §1.56(b).

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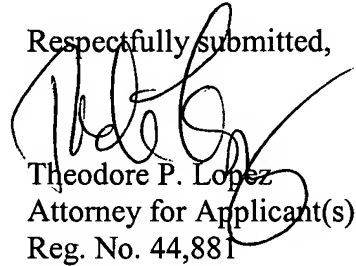
Applicant(s) believes that no fee is required for submission of this statement since it is being submitted prior to the first office action. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 50-2257. Please deduct any additional fees from, or credit any overpayment to the above-noted Deposit Account.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on February 23, 2004.


Sandy Kim

February 23, 2004
Date of Signature

Respectfully submitted,


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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No.	
				M-15267 US		10/718,449	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s)			
(Use several sheets if necessary)				Jishnu Bhattacharjee et al.			
				Filing Date		Group	
				November 19, 2003		Unassigned	

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,395,637 B1	05/28/2002	Park et al.	438	706	
	AB	6,593,838 B2	07/15/2003	Yue	336	84	
	AC						
	AD						
	AF						
	AF						

Foreign Patent Documents							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AG							
	AH							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
AI	H.Y. D. Yang "Design Considerations of Differential Inductors in CMOS Technology", Dept. of ECE, University of Illinois at Chicago.	
AJ		

Examiner	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.